

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	228283	nonvolatile or non-volatile	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/15 11:33
L2	0	1 and plurality adj of adj blocks	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/15 11:34
L3	12150	1 and plurality with blocks	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/15 11:34
L4	0	3 and each adj block	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/15 11:34
L5	300	3 and block with memory adj cell and reference adj cell	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/15 11:35
L6	2	5 and reference adj load	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/15 11:36
L7	146	5 and load	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/15 11:36
L8	2	6 and load with reference adj signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/15 11:37

L9	941	365/185.21.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/15 11:37
L10	25	9 and reference adj load	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/15 11:37
L11	20	10 and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/15 11:43
L12	4498859	plurality of reference adj load adj circuits with imposes adj load with reference adj signal with identical. clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/15 11:45
L13	0	plurality adj of adj reference adj load adj circuits with imposes adj load with reference adj signal with identical.clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/15 11:45